Patent Attorney's Docket No.: 42P17187

Confirmation No.: 7668

IN THE CLAIMS

Please cancel claims 24-30.

This listing of claims will replace all prior versions, and listings, of claims

in the application:

(Original) An electronic assembly comprising: 1.

a substrate;

a support plate; and

a plurality of support members, each having a first portion attached to the

substrate and second portion attached to the thermally conductive body, the

support member being thermally separated from the support plate.

2. (Original) The electronic assembly of claim 1, further comprising an

insulating body interconnecting the support plate and the support members.

3. (Original) The electronic assembly of claim 2, wherein the insulating body

is formed on the support plate.

(Original) The electronic assembly of claim 3, wherein the insulating body 4.

is in a recess on the support plate.

5. (Original) The electronic assembly of claim 4, wherein the second portion

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of the support member is embedded in the insulating body.

6. (Original) The electronic assembly of claim 5, wherein the insulating body

is at least one of a ceramic and a liquid crystal polymer.

7. (Original) The electronic assembly of claim 6, wherein the substrate is a

circuit board.

(Original) The electronic assembly of claim 7, wherein the support plate is 8.

metal.

9. (Original) The electronic assembly of claim 8, wherein the support plate is

a heat sink.

10. (Original) The electronic assembly of claim 9, further comprising a

package substrate attached to the circuit board and a microelectronic die

mounted on the package substrate, the heat sink being thermally connected to

the microelectronic die.

11. (Original) The electronic assembly of claim 10, wherein the heat sink is on

a side of the package substrate opposing the circuit board.

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12. (Original) The electronic assembly of claim 11, wherein the package

substrate includes a plurality of contact formations on a bottom surface thereof,

the contact formations interconnecting the package substrate and the circuit

board.

13. (Original) The electronic assembly of claim 12, wherein the

microelectronic die is a microprocessor.

14. (Original) The electronic assembly of claim 13, further comprising a

thermal interface material interconnecting the microelectronic die and the heat

sink.

15. (Original) An electronic assembly comprising:

a circuit board;

a package substrate attached to the circuit board;

a microelectronic die mounted to the package substrate;

a thermally conductive plate thermally connected to the microelectronic

die;

a plurality of insulating bodies attached to the thermally conductive plate;

and

a plurality of support members having first and second portions, the first

portions attached to the circuit board, the second portions attached to the

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insulating bodies.

16. (Original) The electronic assembly of claim 15, further comprising solder

between the circuit board and the package substrate and between the circuit

board and the support members and wherein the support members are

thermally separated from the conductive plate such that when the circuit board

is heated to a temperature to reflow the solder between the circuit board and the

package substrate, a sufficient amount of heat remains in the support members to

reflow the solder between the support members and the circuit board.

17. (Original) The electronic assembly of claim 16, wherein the support

members are metal pins.

(Original) The electronic assembly of claim 17, wherein the metal pins are 18.

soldered to the circuit board.

(Original) The electronic assembly of claim 18, wherein the circuit board 19.

is a motherboard.

20. (Original) The electronic assembly of claim 19, wherein the

microelectronic die is a microprocessor.

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(Original) The electronic assembly of claim 20, wherein the thermally 21.

conductive plate is a heat sink.

22. (Original) The electronic assembly of claim 21, wherein the heat sink is

over the package substrate.

(Original) The electronic assembly of claim 22, further comprising a 23.

thermal interface material interconnecting the heat sink and the microelectronic

die.

24-30. (Cancelled)

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